ABSTRACT OF THE DISCLOSURE

A coprocessor data access control method having a coprocessor memory access instruction with a coprocessor indicating field such that the quantity of word data to be

5   transmitted between the coprocessor and the memory unit can be determined. The coprocessor indicating field actually includes a coprocessor number field and a coprocessor register field. The coprocessor number field indicates the particular coprocessor to be used while the coprocessor register field indicates the particular registers to be used.

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